### Implementation of WiFiRe PHY Sectorization in OPNET

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### Outline

- WiFiRe overview.
- WiFiRe model design in OPNET
- Simulation results of WiFiRe model design.
- Conclusion and future work.

System Architecture Sectorization and frame structure MAC services

### System Architecture

- WiFiRe adopts star network topology.
- System is designed to cover cell with radius 15-20Km
- Cell is divided in to sectors.
- System S consists of set of BSs
- ST antenna is directional which minimizes co-channel interference





System Architecture Sectorization and frame structure MAC services

### Sectorization and frame structure

- Downlink and Uplink transmissions in a sector.
- WiFiRe employs TDD-MSTDM scheduling of slots
- Time is divided in to Frames

#### Protocol phases

- Network entry and initialization
  - Ranging
  - Registration



#### Figure: Uplink Downlink frame timings

System Architecture Sectorization and frame structure MAC services

#### MAC services

- ► UGS, pre-allocates periodic transmission opportunities to the STs, which eliminates the overhead involved in the bandwidth request-grant process. Used for fixed size periodic transmission such as VoIP.
- rtPS, ensures that STs get periodic bandwidth request opportunities. The STs can then request bandwidth from the BS.
- nrtPS, designed to support high bandwidth connections and not delay sensitive, which require variable size data grant slots on a regular basis, such as high bandwidth FTP.
- Best Effort service targets best effort traffic where no throughput or delay guarantees are necessary. These flows served in contention slots.

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

### Prior work done on WiFiRe

#### Initial contributions 1

- Implemented some Base station, Subscriber station specific functionalities like processing control packets, beacon processing etc.
- Greedy hueristic scheduler at Base station with optimization parameter of UGS grant size of subscriber station.

#### Initial contributions 2

- Implemented WiMAX MAC model modifying for WiFiRe
- Modelled Polling, Bandwidth request mechanisms to simulate rtPS and nrtPS flows
- Modelled Round robin scheduler to accept various flows.

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

### WiFiRe model design

#### Overview of work done earlier



#### Figure: WiFiR model design

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

### WiFiRe Common MAC process model

# Common functionalities to ST and BS

- segmentation and reassembly
- Beacon processing
- packet classification



#### Figure: WiFiRe common MAC process

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

#### ST child control process

ST child control process responsible for, DSA req, DSA response

#### Base child control process

- sending beacon periodically
- Processing DSA request
- Sending DSA response
- Scheduling downlink traffic

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

### Present Work

#### Subscriber node model

- All STs have same MAC process
- MAC process responsible for defining service flows, processing beacons
- Each ST is modelled with directional antenna pointed towards system S

#### Base station node model

- BS node model consists of 6 WiFiRe interfaces, each WiFiRe interface represents a sector.
- Each WiFiRe interface is connected to antenna through transmitter and receiver.





WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

### Antenna object with transmitter and receiver

- Radio receiver and Radio trasmitter are entry and exit points of packets through wireless environment via antenna object
- Antennas are used for modeling radio transmission
- Antenna pattern editor allow modeling of directional gain at radio receivers and transmitters



Figure: Antenna object with Tx and Rx

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### Antenna pattern

- Antenna pattern is defined in terms of spherical angle phi and theta
- Phi is horizontal angle
  - If phi = 0 then the point is on the positive z-axis
  - If phi = pi / 2 then the point is in the xy-plane
  - if phi = 180 then the point is on the negative z-axis
- Theta measures the angle from the positive xz-plane to the point.
- 3D antenna pattern is collection of 2D slices.
- Slices used for defining gain pattern



Figure: Antenna pattern using spherical angle phi and theta

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### Antenna pattern editor

- Antenna pattern editor is used for antenna pattern
- For each 2D slice, absicca is theta and ordinate is associated gain
- Gain tables are created for 2D slices by sample points
- For n slices there are 2n sample points



Figure: Antenna pattern editor for creating antenna pattern

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### Antenna Pointing processor module

- Each antenna is associated with processor module.
- Calculates the information that antenna needs to point at target.
- Convert node's position in to subnet
- Associates receiver node with tranmitter in the subnet through antenna object
- Processor module makes calculation using Kernal procedures.

```
/* op_topo_parent() uses the processor object ID to determine the
/* processor's parent object ID. */
rx_node_id = op_topo_parent (op_id_self ());
subnet_id = op_topo_parent (rx_node_id);
tx_node_id = op_id_from_name (subnet_id, OPC_OBJTYPE_NDFIX, "tx");
comp_code = op_ima_obj_pos_get (tx_node_id, &latitude, &longitude,
&altitude, &x_pos, &y_pos, &z_pos);
rx_ant_id = op_id_from_name (rx_node_id, OPC_OBJTYPE_ANT, "ant_rx");
```

Figure: Processor module code snippet

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

### Pipeline stages

- Exists between radio transmitter and radio receiver.
- Implements physical layer characteristics of wireless medium.
- Executed once for each packet transmission.
- TDA's for communication between pipeline stages



Figure: Transmission of packet



Figure: Transmitter pipeline stages



Figure: Receiver pipeline stages

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

#### Transmission delay

- Calculate the time for the entire packet transmission
- Active the trace to check the labelled break point is achieved
- Get transmission data rate from the packet using KP
- > Place the result in Transmission data attributes for use of latter stages

```
if (op_prg_odb_ltrace_active ("sree"))
    op_prg_odb_print_minor ("reached transmission delay", OPC_NIL);
op_prg_odb_bkpt ("sree_test_bk_pt");
/*code by sree*/
op_pk_nfd_get_dbl (pkptr, "Tx Data Rate", &tx_drate);
/* Obtain length of packet. */
pklen = op_pk_total_size_get (pkptr);
/* Compute time required to complete transmission of packet */
    tx_delay = pklen / tx_drate;
op_td_set_dbl (pkptr, OPC_TDA_RA_TX_DELAY, tx_delay)
```

Figure: Transmission delay code snippet

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

#### Channel match stage

- Active the trace to check labelled break point is acheived.
- Check for non overlapping bands for classification.
- > Place the result in Transmission data attributes for use of latter stages

```
if (op prg odb ltrace active ("sree test"))
    op prg odb print minor ("reached channel match pipeline stage", OPC NIL);
op prg odb bkpt ("sree test bk pt");
/* Obtain transmitting channel attributes. */
tx freq = op td get dbl (pkptr, OPC TDA RA TX FREQ);
tx bw
            = op td get dbl (pkptr, OPC TDA RA TX BW);
/* Obtain receiving channel attributes. */
rx freq
          = op td get dbl (pkptr, OPC TDA RA RX FREQ);
rx bw
          = op td get dbl (pkptr, OPC TDA RA RX BW);
if ((tx freq >= rx freq + rx bw) || (tx freq + tx bw <= rx freq))</pre>
op td set int (pkptr, OPC TDA RA MATCH STATUS, OPC TDA RA MATCH IGNORE);
if ((tx freq != rx freq) || (tx bw != rx bw))
op td set int (pkptr, OPC TDA RA MATCH STATUS, OPC TDA RA MATCH NOISE);
```

Figure: Channel match stage code snippet

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

#### Transmitter antenna gain

- Compute the gain associated with the transmitter's antenna.
- Get all the transmitter geocentric coordinates of transmitter.
- perform difference vector from the transmitter to receiver.
- > Place the result in Transmission data attributes for use of latter stages

```
/** Compute the gain associated with the transmitter's antenna. */
/* Obtain the geocentric coordinates of the transmitter. */
tx_x = op_td_get_dbl (pkptr, OPC_TDA_RA_TX_GEO_X);
tx_z = op_td_get_dbl (pkptr, OPC_TDA_RA_TX_GEO_Y);
tx_z = op_td_get_dbl (pkptr, OPC_TDA_RA_TX_GEO_Z);
/* Compute the vector from the transmitter to the receiver. */
dif_x = rx_x - tx_x;
dif_y = rx_y - tx_y;
dif_z = rx_z - tx_z;
/* converts antenna direction to gain value corresponding to that direction*/
gain = op_tbl_pat_gain (pattern_table, rx_phi, rx_tbeta);
/* Set the tx antenna gain in the packet's transmission data attribute. */
op_td_set_dbl (pkptr, OPC_TDA_RA_TX_GAIN; gain);
```

Figure: Transmitter antenna gain code snippet

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

#### **Propagation delay**

- Get distance between transmitter and receiver using TDA
- Propagation velocity is set to propagation velocity of raido signal.
- Calculate propagation delay using propagation velocity and propagation distance
- The result is placed in the propagation delay TDA

```
/** Compute the propagation delay between the transmitter and receiver**/
/* Get the distance between transmitter and receiver. */
prop_distance = op_d_get_dbl (pkptr, OPC_TDA_RA_START_DIST);
/* Compute propagation delay to start of reception. */
prop_delay = prop_distance / PROP_VELOCITY;
op td set dbl (pkptr, OPC TDA RA_START PROPDEL, prop_delay)
```

Figure: Propagation delay code snippet

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

#### **Received power**

- Compute the received power of the arriving packet
- Correct reception of packet is determined by channel match.
- Depends on path loss, transmission frequency, transmitter and receiver antenna gain.
- Result is placed in TDA for use of latter stages.

#### Interference noise

- Account for the situation that two packets arrive concurrently at the same receiver channel
- Increment the number of collisions for two packets
- Interference of previous packet on arriving one is calculated and placed in TDA.

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

#### Signal to noise ratio

- Receive the rcvd power and interference noise from previous stages
- Calculated snr using received power and noise in db

```
/** Compute the signal-to-noise ratio for the given packet. **/
/* Get the packet's received power level. */
rcvd_power = op_td_get_dbl (pkptr, OPC_TDA_RA_RCVD_POWER);
if (op_prg_odb_brint_minor ("reached snr", OPC_NIL);
op_prg_odb_bkpt ("sree_test_bk_pt");
/*code by sree*/
/* Get the interference noise from previous stage */
accum_noise = op_td_get_dbl (pkptr, OPC_TDA_RA_NOISE_ACCUM);
/*Assign Snr in dB */
op_td_set_dbl (pkptr, OPC_TDA_RA_SNR, 10*log10(rcvd_power/(accum_noise)))
```

Figure: Signal to noise ratio code snippet

WiFiRe model design Components in Wifire model Antenna pattern Opnet Pipeline stages

#### Bit error rate

- From Modulation scheme of Bit rate and SNR it calculates bit error rate
- ▶ Bit error rate is placed in BER Transmission date attribute
- Determine whether or not the arriving packet can be accepted

```
/* Determine current value of Signal-to-Noise-Ratio (SNR). */
    if (op_prg_odb_ltrace_active ("sree"))
    op_prg_odb_print_minor ("reached ber", OPC_NIL);
    op_prg_odb_bkpt ("sree_test_bk_pt");
    snr = op_td_get_dbl (pkptr, OPC_TDA_RA_SNR);
    /* Derive expected BER from effective SNR. */
    ber = op_tbl_mod_ber (modulation_table, snr);
    /* Place the BER in the packet's transmission data. */
    op td set dbl (pkptr, OPC TDA_RA_BER, ber);
```

Figure: Bit error rate code snippet

Sector deployment using antenna pattern Experiment to simulate VoIP calls Experiment to simulate VoIP and Video scenario

### Experiments

- Building WiFiRe model in OPNET.
- Setup a sector using directional antenna.
- Setup STs in six sectored system with VoIP configuration
- Experiment to simulate VoIP and Video scenario

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S.No	Parameter	Value	
1	Data Rate	11 Mbps	
2	Control packet data rate	2 Mbps	
3	Frame Duration	10 ms	
4	Slot duration	$32\mu$ sec	
5	DL-UL ratio	2:1	
7	Service class	UGS	

#### Table: System setup parameters

S.No	Parameter	Value	
1	Frequency Channel	2.4 GHz	
2	Bandwidth	22 MHz	
3.	Duplexing technique	TDD	
4.	PHY overhead	96 $\mu$ sec	
5.	Number of symbols/frame	220000	
6.	Symbol duration	$0.045 \mu sec$	

Table: PHY profile setup parameters

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### Setup to perform range of sectorization using directional antennas

Purpose of this experiment to show the sectorization works





Figure: Scenario 1 with unit distance of 5Km

Figure: Scenario 2 with unit distance of 5Km

Above scenarios has setup of 60 degrees sectorized antenna at base station

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### Performance plots for range of sectorization



Figure: Load for scenario 1



Figure: Throughput for scenario 1



Figure: Throughput for scenario 2

### Throughput of sectorization using directional antennas

- Each ST is requested with 20Kbps
- The total load offered with first scenario is 60Kbps.
- The total load offered with second scenario is 100Kbps.
- Throughput for first scenario with 3 STs is same as load due to proper sectorization of BS with respect to directionality of ST.
- ► The load offered for scenario 2 is 100Kbps.
- Throughput for Scenario 2 is less compared with load as some STs in this scenario are out of range in the sector.

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### Sectorized deployed in a sector using antenna pattern

Global Tables	Preview			
Object Tables	Service Suscriber	Direction	Class	Scheduling T
Campus Network	ST2	Uplinik	Platinum	BE
	ST2	Uplink	Gold	UGS
WARe	ST2	Uplink	Silver	#PS
<ul> <li>BS Admission Control Statistics</li> </ul>	ST2	Uplink	Bronze	ntPS
<ul> <li>BS Admitted Connections</li> </ul>	ST2	Downlink	Platinum	BE
<ul> <li>BS Rejected Connections</li> </ul>	ST2	Downlink	Gold	UGS
	ST2	Downlink	Silver	#PS
	ST2	Downlink	Bronze	ntPS
	ST1	Uplink	Platinum	BE
	ST1	Uplink	Gold	UGS
	ST1	Uplink	Silver	#PS
	ST1	Uplink	Bronze	ntPS
	ST1	Downlink	Platinum	BE
	ST1	Downlink	Gold	UGS
	ST1	Downlink	Silver	#PS
	ST1	Downlink	Bronze	ntPS
	ST3	Uplink	Platinum	BE
	ST3	Uplink	Gold	UGS
	ST3	Downlink	Platinum	BE
	ST3	Downlink	Gold	UGS
	ST3	Downlink	Silver	#PS -
	ST3	Downlink	Bronze	ntPS



Figure: Admitted connections for scenario 1

Figure: Rejected connections for scenario 2

Some connections are rejected in scenario 2 due to out of range in the sector

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### Experiment to simulate VoIP calls



Figure: Scenario with 40 STs and unit distance of 5 Km



Figure: Scenario with 120 STs and unit distance of 5  ${\rm Km}$ 

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### Performance plots for experiment to simulate VoIP calls



- Each ST requesting Bandwidth of 120Kbps
- Requested rate with MACoverhead is 130Kbps
- Load is 120Kbps, Actual traffic sent is 130Kbps

Figure: Load of the system



Figure: Actual traffic sent

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### Theoritical number of VoIP calls supported

Original Bandwidth request from ST = 120KbpsRequested rate with MACoverhead =  $rate \times (\frac{avg\_pkt\_size + WiFiRe\_header}{avg\_pkt\_size})$ =  $(120 \times 10^3) \times (\frac{60+5}{60}) = 130Kbps$ Requested rate in symbols per second =  $rate_mac \times (\frac{1}{number_of_bits_persymbol \times coding_rate})$  $=(130 \times 10^3) \times (\frac{1}{1 \times \frac{1}{2}}) = 260 \times 10^3$ Number of symbols required per frame = (req rate in sps)  $\times$  (frame duration) symbols per frame =  $260 \times 10^3 \times 10 \times 0.001$  =2600 symbols symbols per slot =  $\frac{32}{0.045}$  = 711 symbols



WiFiRe PHY overhead = 96 micro sec Number of symbols with WiFiRe PHY overhead =  $\frac{PHY\_overhead\_duration}{PHY\_overhead\_duration} = 2133 symbols.$ Symbol\_duration Total number of symbols required is sum of PHY overhead and requested rate = PHY overhead + Number of symbols requested = 2133+2600=4733 symbols Total\_number\_of\_slots Total number of slots =symbols\_for\_slot  $=\frac{4733}{711}=6$ Down link bandwidth =  $\frac{66}{100} \times 11 * 10^{6} = 7.26 \times 10^{6}$ With 40 ST's ratio each ST has 323 Kbps if voice packet server in two frames  $=\frac{7260Kbps}{40}\times2=323Kbps$ similarly with 120 STs it is around 119 Kbps which is close to requested bandwith

Sector deployment using antenna pattern Experiment to simulate VoIP calls Experiment to simulate VoIP and Video scenario

### Traffic received and Throughput for 40 STs

- All STs got Bandwidth alloted with theoritical calculation of 66 percentage DL Bandwidth
- Throughput is equal to traffic sent
- Each ST has 3 data slots and 3 phy slots
- Three VoIP calls are supported by each ST



Figure: Traffic received for 40 STs



Figure: Throughput for 40 STs

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### Queuing dealy and datadropped for 40 STs



- Arrival rate is equal to service rate
- Queuing delay is constant
- No traffic dropped

Figure: Data drop for 40 STs



Figure: Queuing delay for 40 STs

Sector deployment using antenna pattern Experiment to simulate VoIP calls Experiment to simulate VoIP and Video scenario

### Data dropped and queuing delay for 120 STs



- No sufficient bandwidth alloted to STs
- Queuing delay varies
- Queue will be filled packets are dropped

Figure: Data drop for 120 STs



Figure: Queuing delay for 120 STs

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### Throughput for 120 STs

- Bandwidth not sufficent for STs
- Throughput is less compared to traffic sent

Figure: Throughput for 120 STs

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### Experiment to simulate VoIP and Video scenario



Figure: Scenario with ST and unit distance of 5Km

Sector deployment using antenna pattern Experiment to simulate VoIP calls Experiment to simulate VoIP and Video scenario

## End to End delay for UGS

- This experiment is based on earlier model to simulate VoIP and Video Scenario by changing parameters
- The type of scheduling used is round robin
  - BS first allocates admitted number of slots for UGS flows.
  - Allocates requested number of slots for rtPS, if the requested number of slots are less than the admitted number of slots.
- UGS requesting 120Kbps serving smoothly
- BS allocates requested no of slots to UGS



Figure: End to end delay for UGS

Sector deployment using antenna pattern Experiment to simulate VoIP calls Experiment to simulate VoIP and Video scenario

### End to End delay for rtPS

- rtPS connection requesting 120Kbps
- End to end delay is intially high, as BS allocates admitted slots to UGS
- End to end delay gradually reduces to constant after getting requested number of slots



#### Figure: End to end delay for rtPS

Sector deployment using antenna pattern Experiment to simulate VoIP calls Experiment to simulate VoIP and Video scenario

### Throughput for UGS and rtPS

- Throughput of UGS connection is high intially compared to rtPS
- As BS first allocates admitted number of slots to UGS
- Throughput of rtPS reaches 120Kbps after some time with requested rate



Figure: Throughput of UGS and rtPS

Conclusion Future Work

### Conclusion

- Implemented WiFiRe model in OPNET
- Directional Antennas are modelled
- Pipeline stages are implemented to simulate physical behaviour of wireless link
- Results shows the model is working as expected

### Future Work

- More experiments need to be performed in order to check performance of WiFiRe.
- The Performance questions are to be answered based upon different parameters are
  - What is the maximum number of users per sector and throughput in the sector which depends up on maximum throughput that channel can achieve
  - Number of packets dropped which depends on number of service flows and number of active users in the sector
  - Maximum number of allowed connections per ST

Conclusion Future Work

### Thank You

P Sreedhar Reddy Roll No. 06305024 Implementation of WiFiRe PHY Sectorization in OPNET